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(54) **DATA PROCESSING DEVICE**

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(57) Abstract:

PURPOSE: To obtain a data processing device where variable length instructions are executed in a high speed, by providing specific instruction buffer means, instruction aligner, operation code decoding means, and operand designator decoding means.

CONSTITUTION: An instruction buffer means which reads an instruction from a memory 301, where instructions and operands are stored, and holds it and an instruction aligner means 305 which takes out an instruction including an operand designator from the instruction buffer means are provided. An operation code decoding means which specifies the operand designator included in the taken-out instruction and specifies the function of the instruction and an operand designator decoding means 309 are provided. The instruction aligner means and the operation code decoding means prepare different operands so that operand designators of one or more operands are decoded in one machine cycle, and thus, the pipe-line processing among plural operands is performed.

